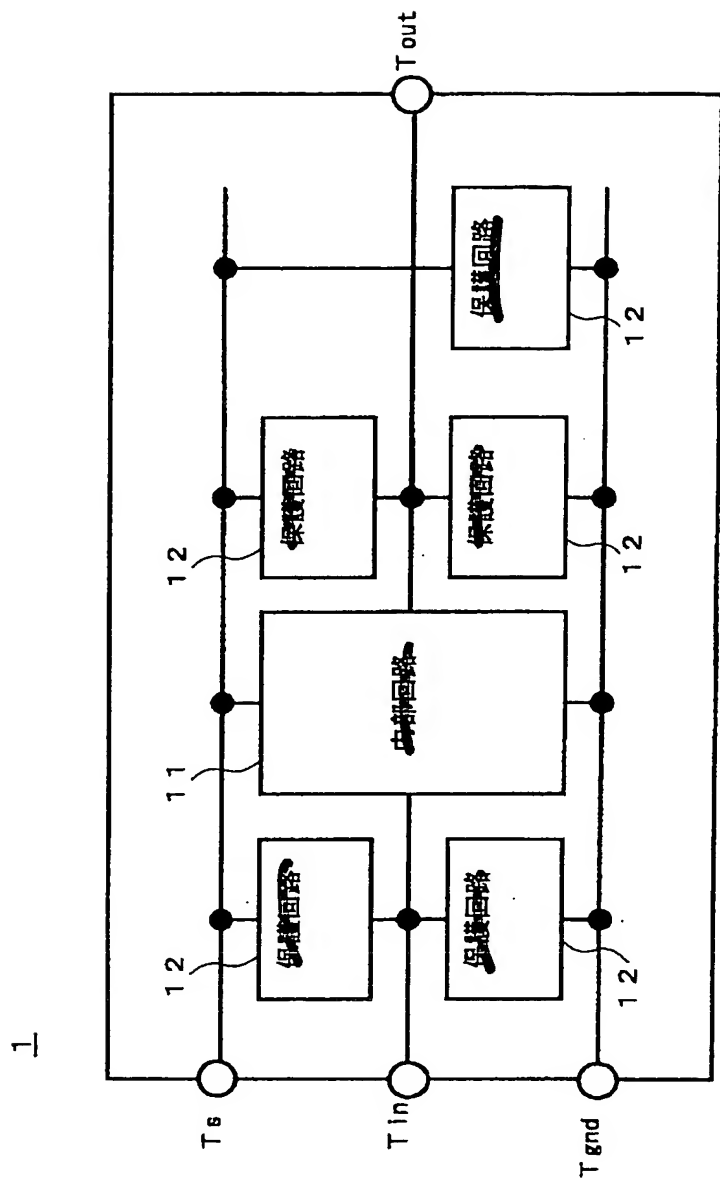


Fig.1



11. INTERNAL CIRCUIT

12. PROTECTION CIRCUIT

Fig. 2

1 2

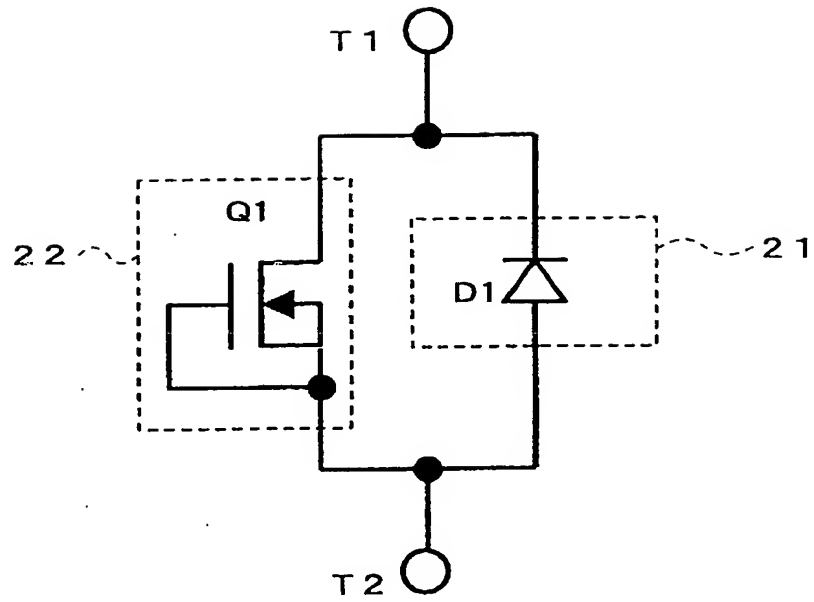


Fig. 3A

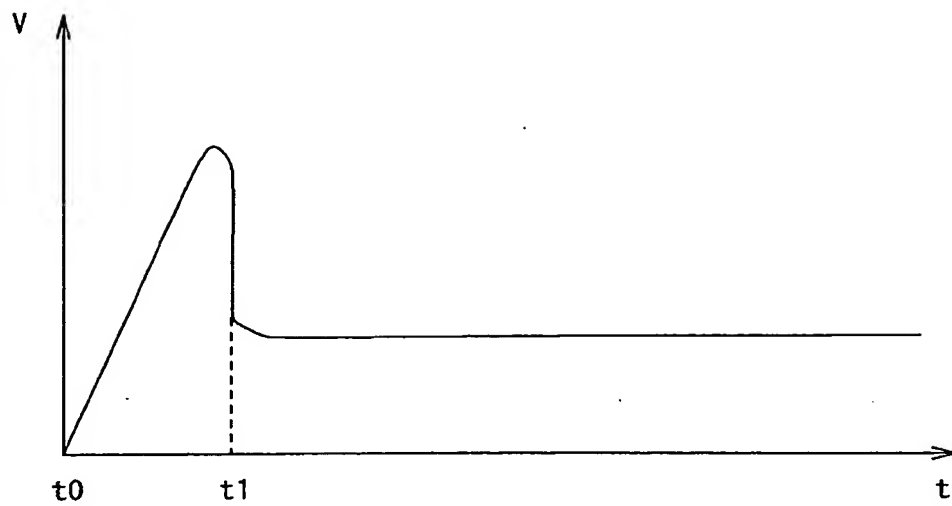


Fig. 3B

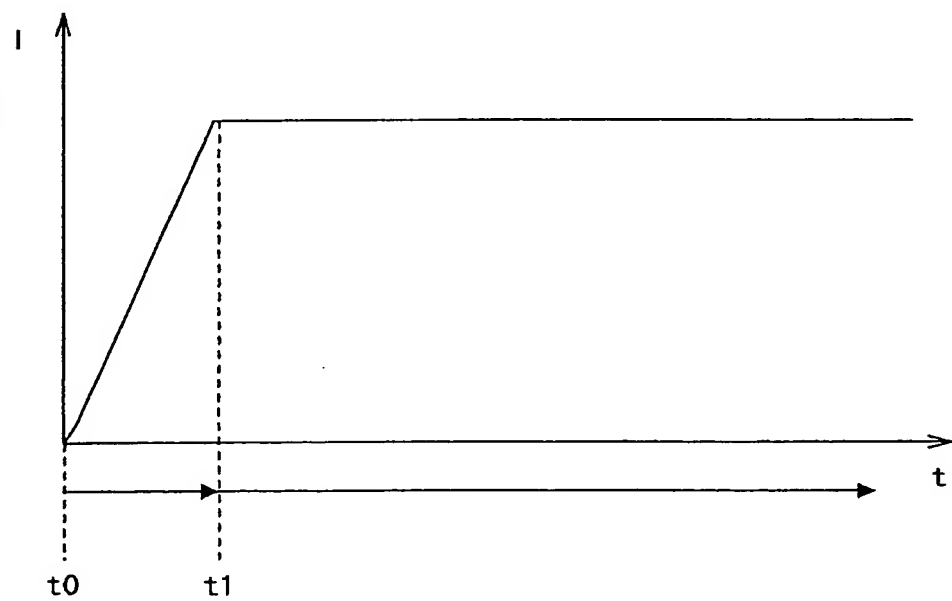


Fig. 4

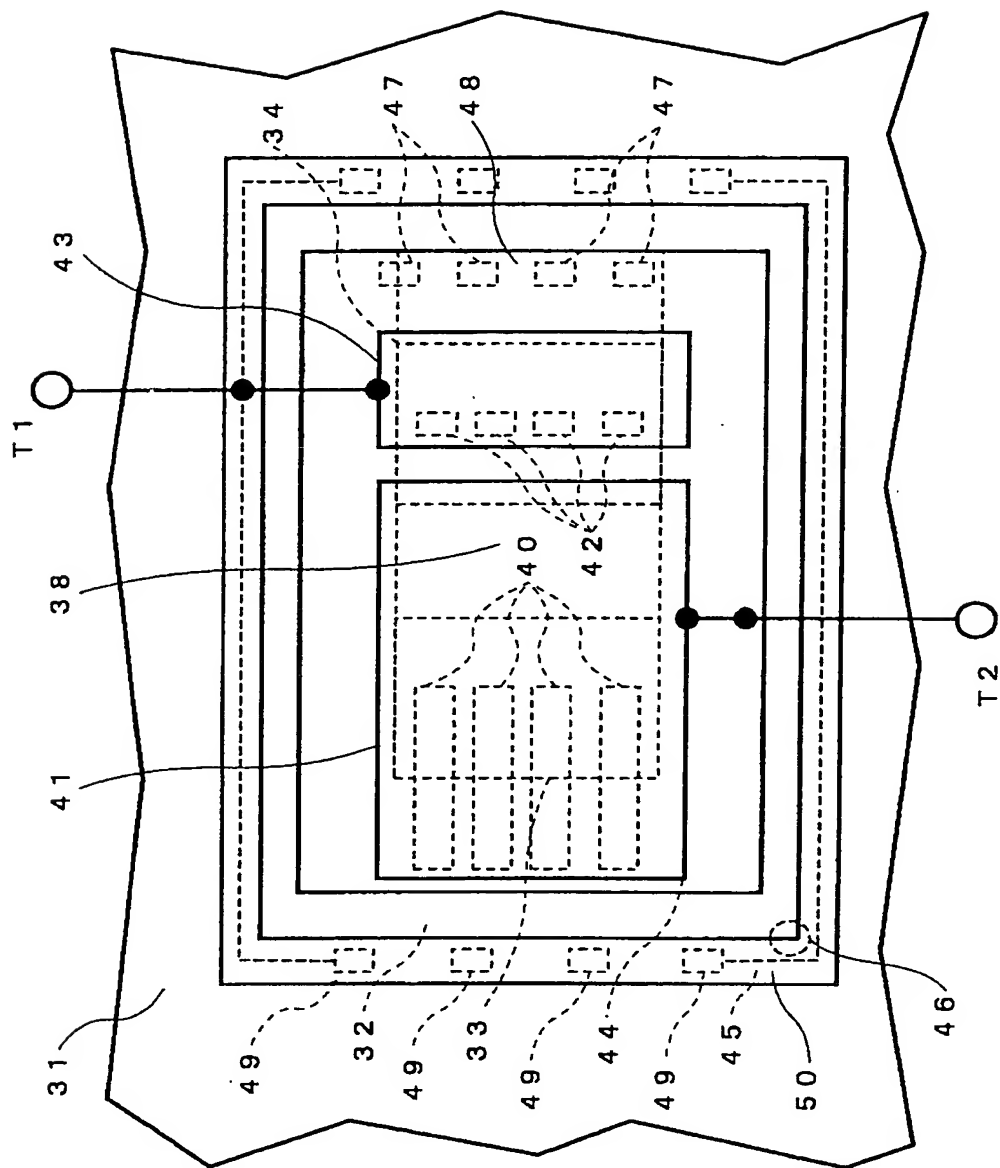


Fig. 5

12

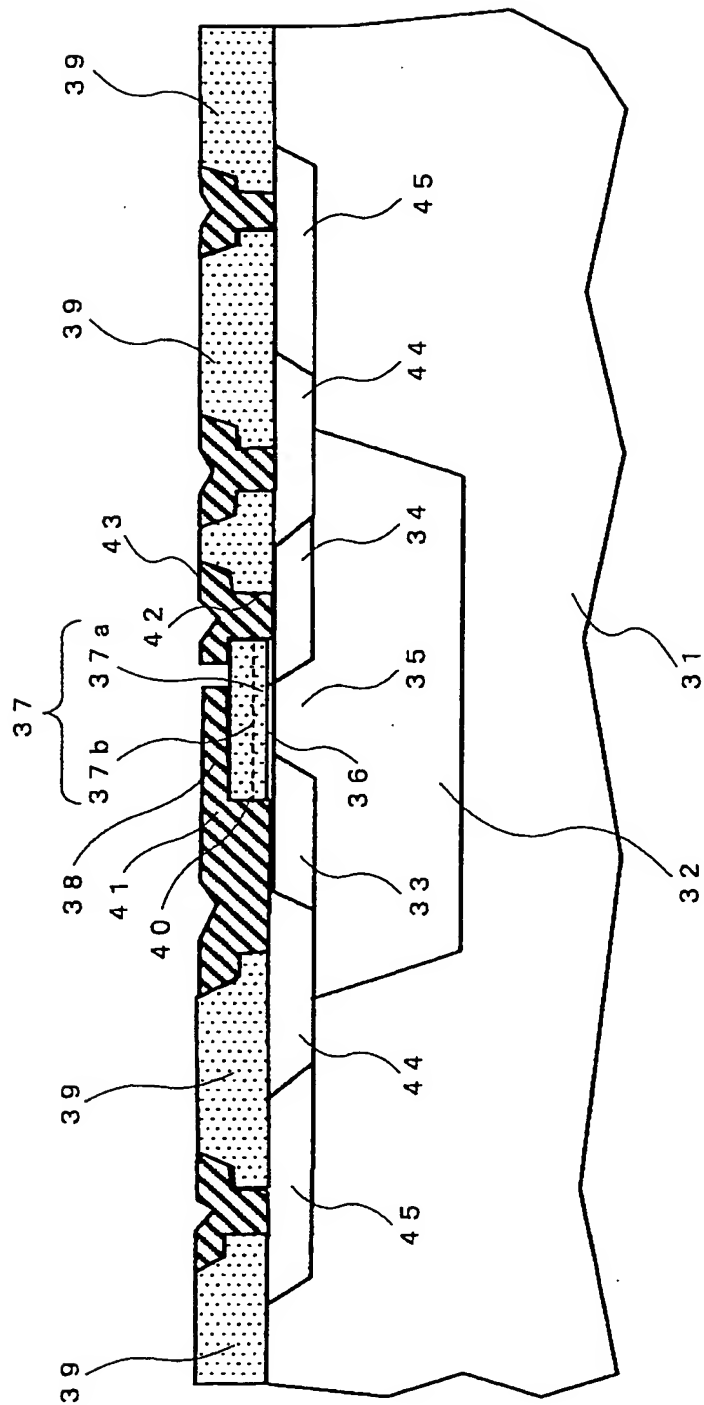


Fig. 6

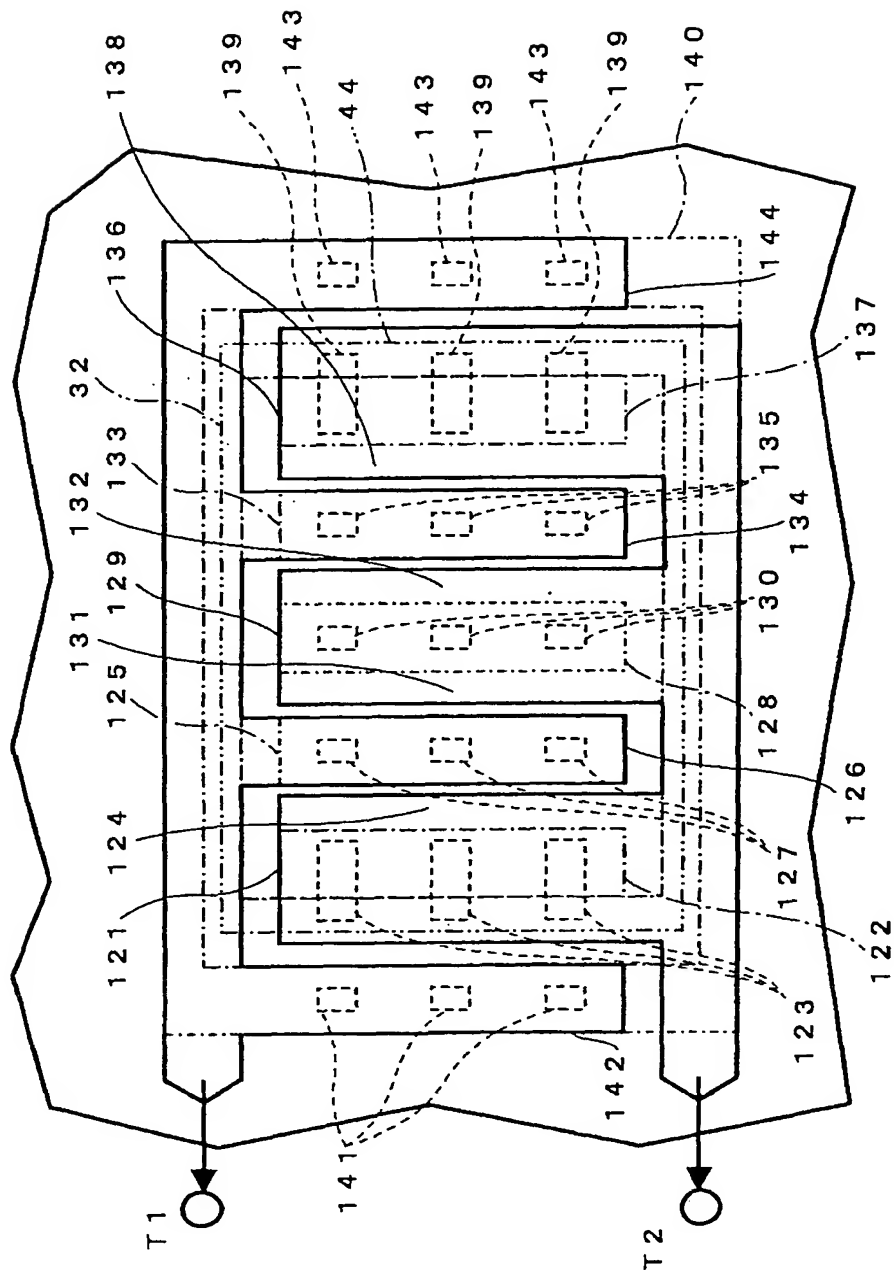


Fig. 1

1/2

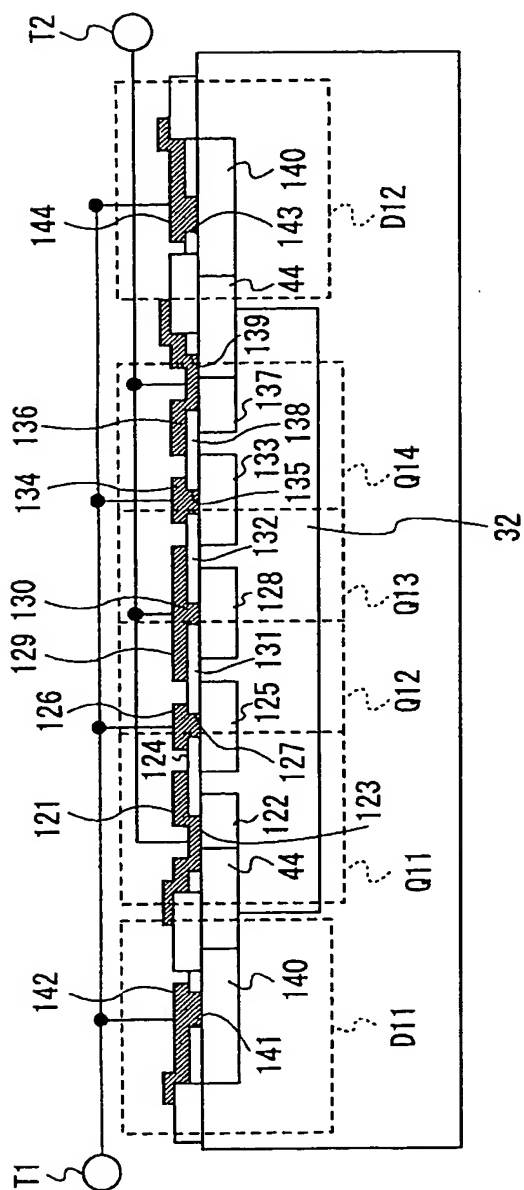


Fig. 8

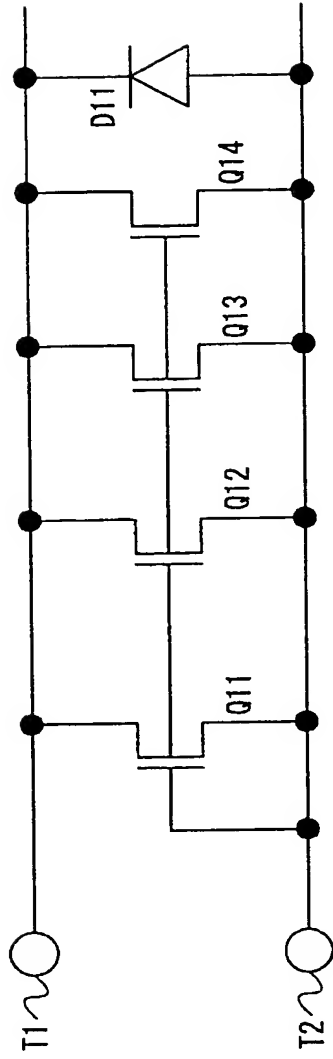
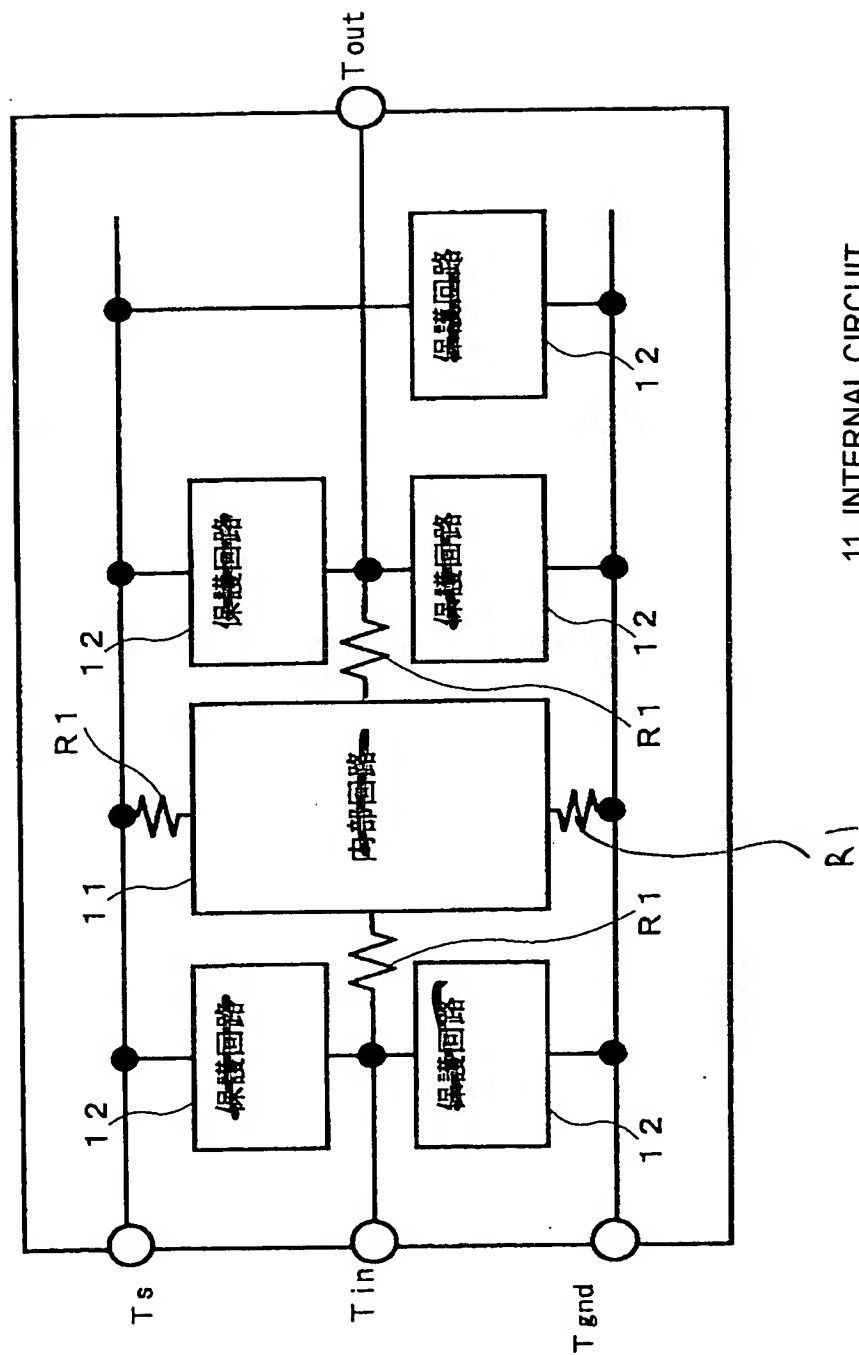




Fig. 9

200



11. INTERNAL CIRCUIT

12. PROTECTION CIRCUIT

Fig. 10

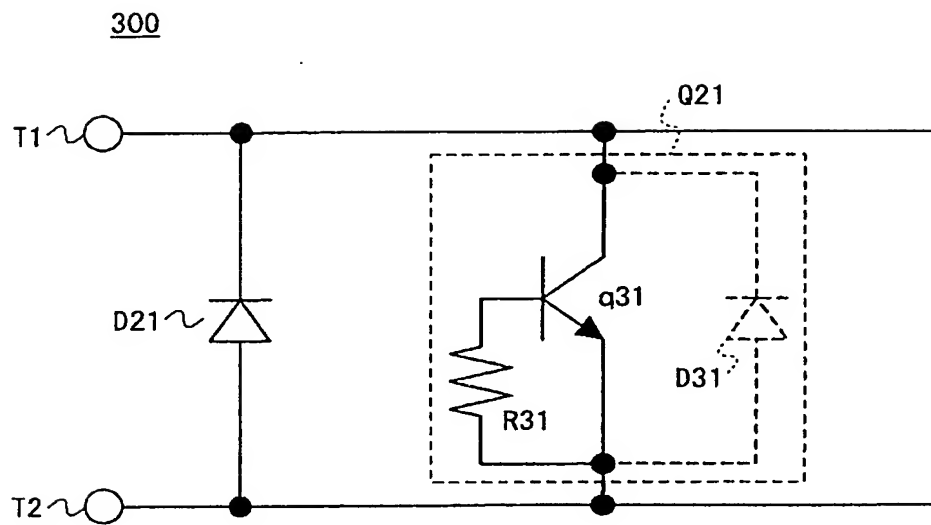


Fig. 11

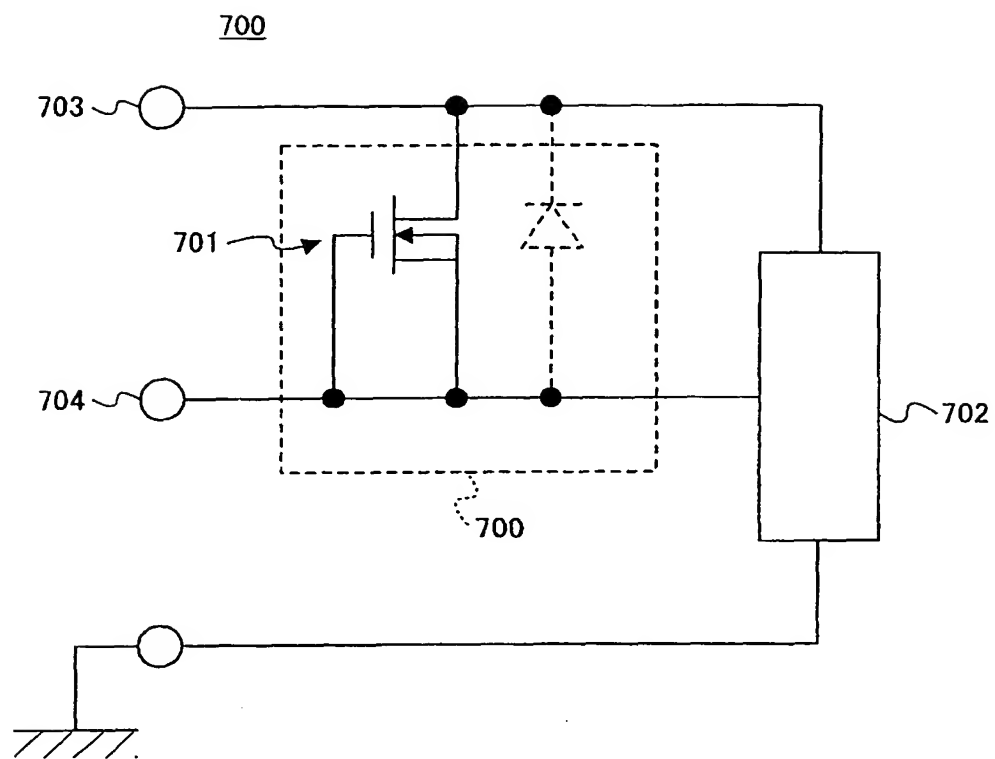


Fig. 12

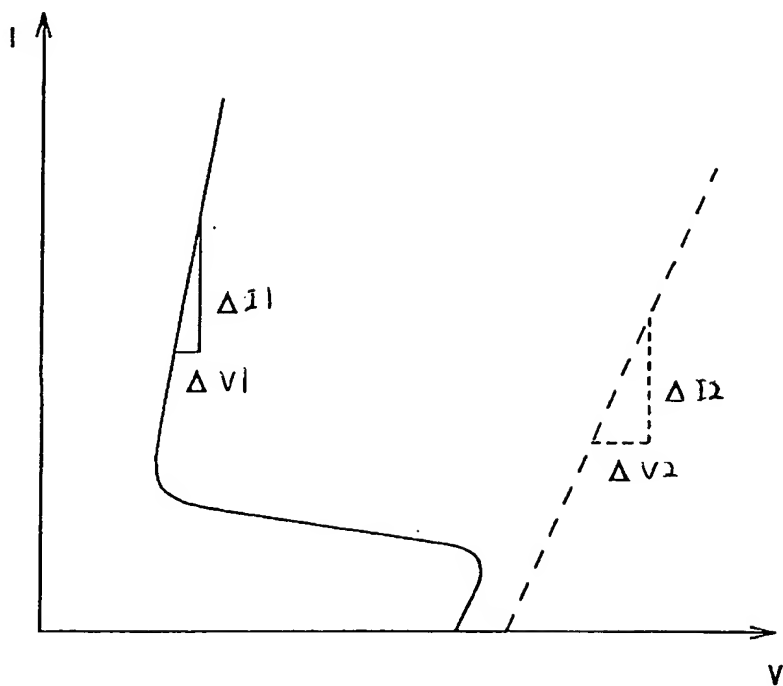


Fig. 13A

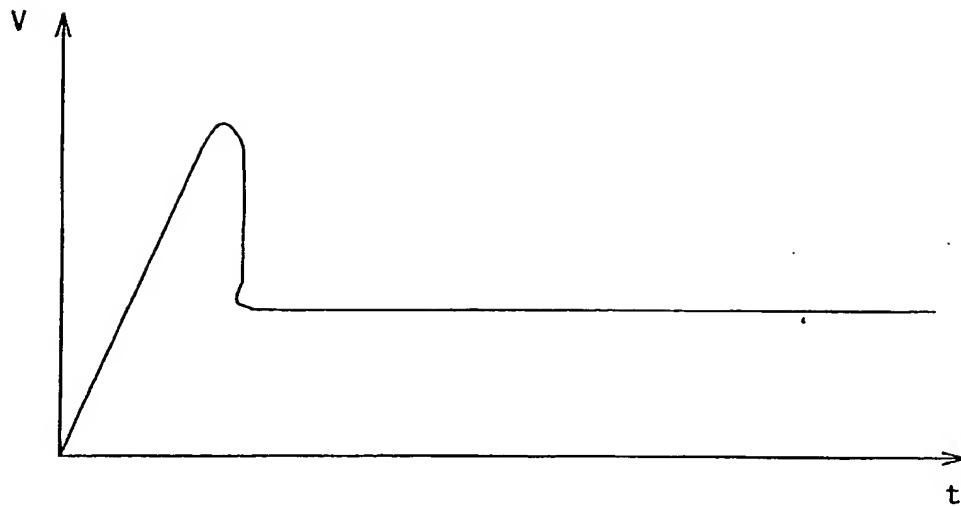


Fig. 13B

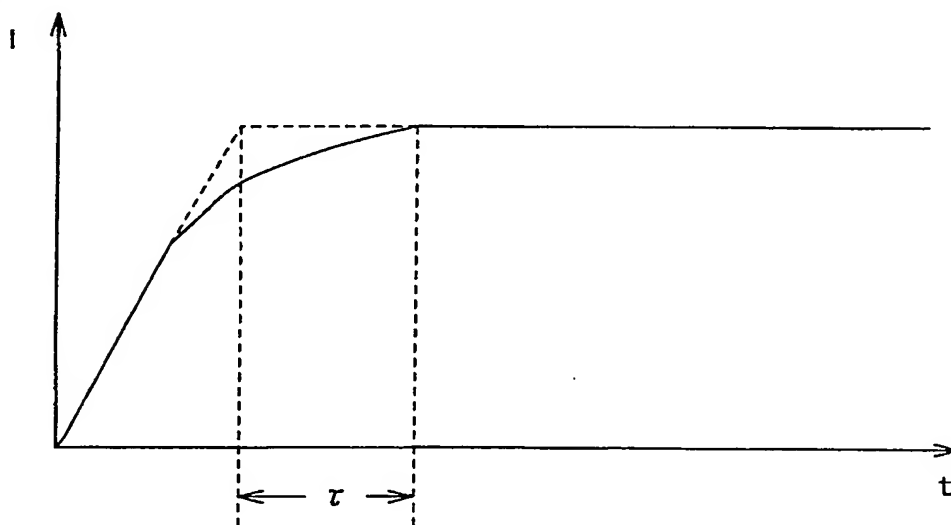


Fig. 14

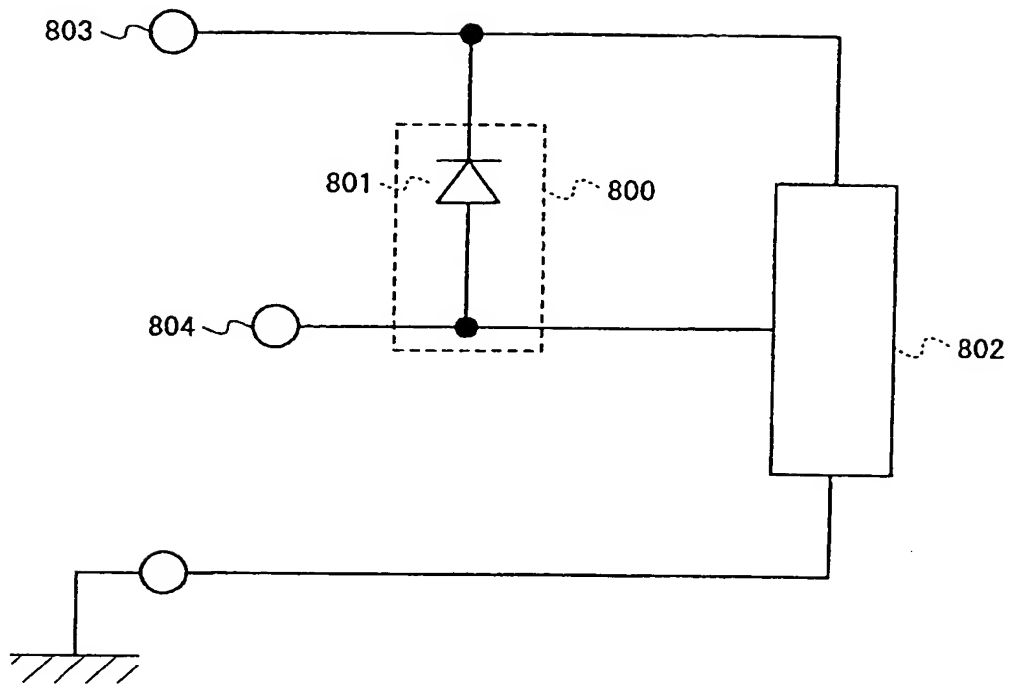


Fig. 15A

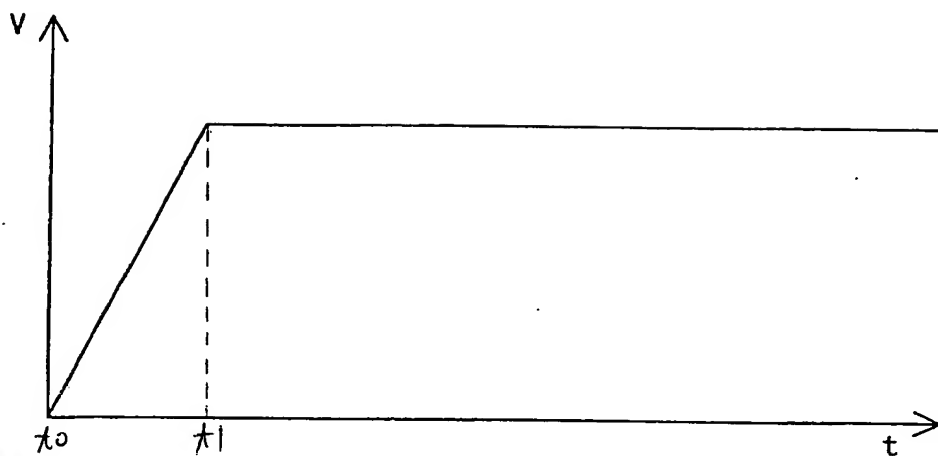


Fig. 15B

